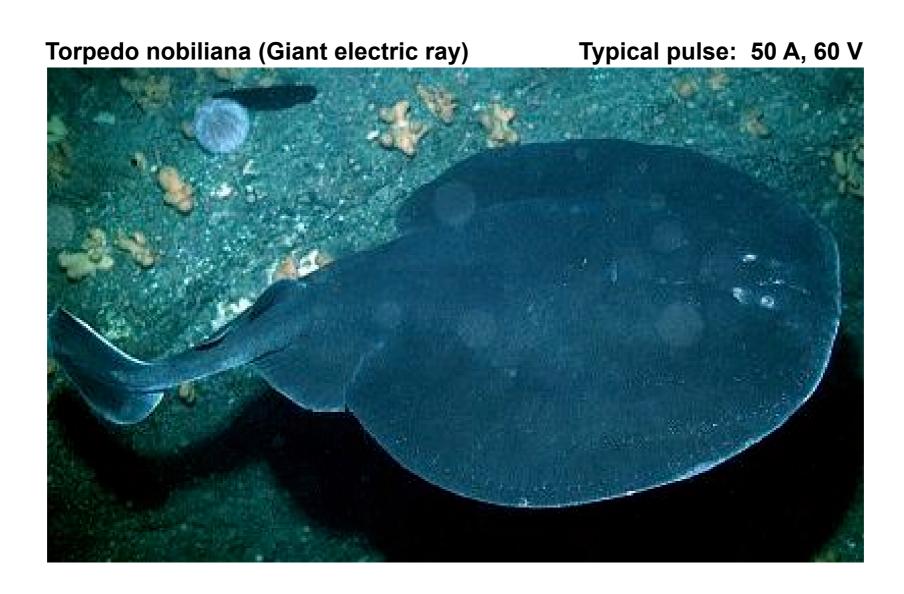
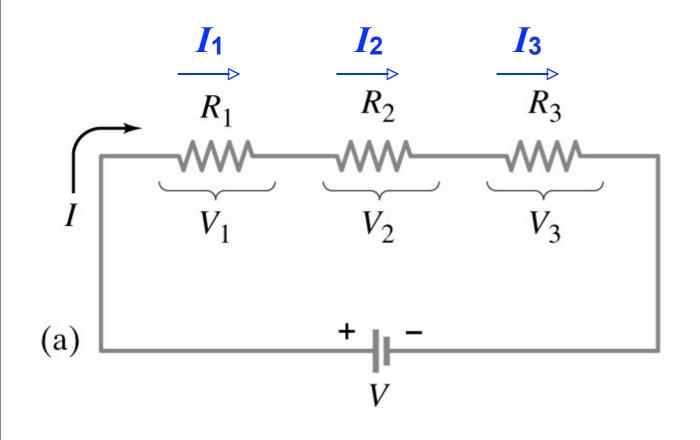
## Physics 590B

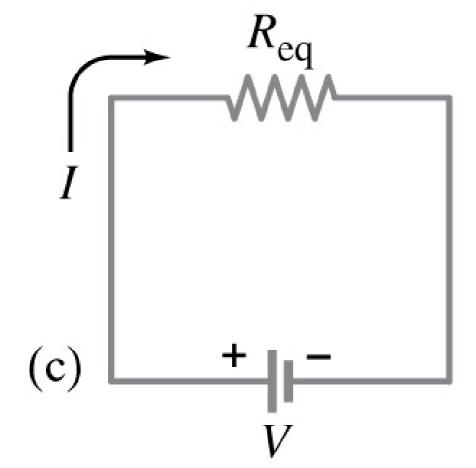
# Electrical signals generation and measurements



### Connecting resistors in series



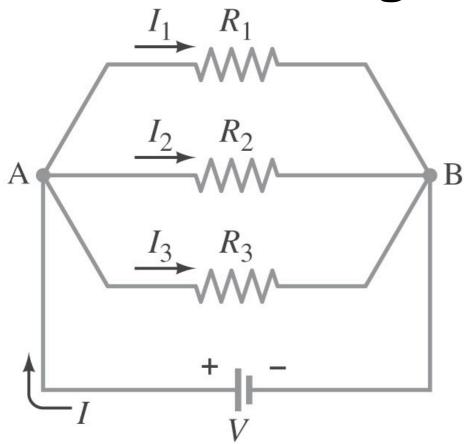
$$I_1 = I_2 = I_3 = I$$
 $V_1 = IR_1 \quad V_2 = IR_2 \quad V_3 = IR_3$ 
 $V = V_1 + V_2 + V_3$ 



$$V = IR_{
m eq}$$
 (same I and V as before)

$$R_{\rm eq} = R_1 + R_2 + R_3$$

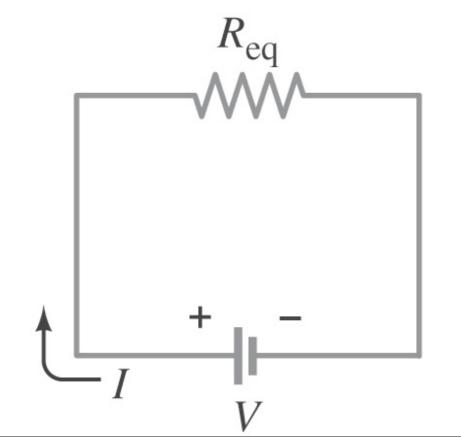
### Connecting resistors in parallel



$$I = I_1 + I_2 + I_3$$

$$V = V_1 = V_2 = V_3$$

$$V = I_1R_1 \quad V = I_2R_2 \quad V = I_3R_3$$



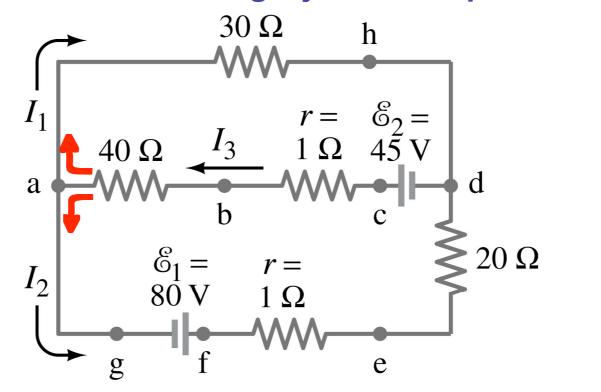
$$V = IR_{\mathrm{eq}}$$
 (same I and V as before)

$$\frac{1}{R_{\text{eq}}} = \frac{1}{R_1} + \frac{1}{R_2} + \frac{1}{R_3}$$

### Kirchhoff's rules

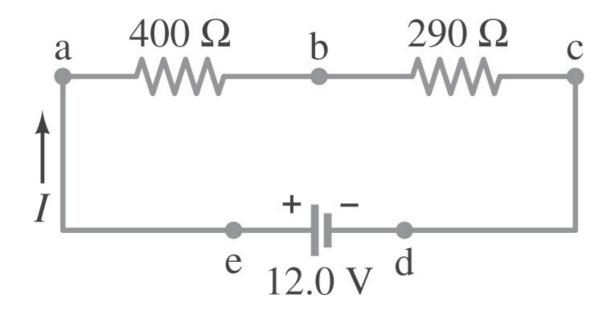
Junction rule: Sum of currents entering a junction equals the sum of the

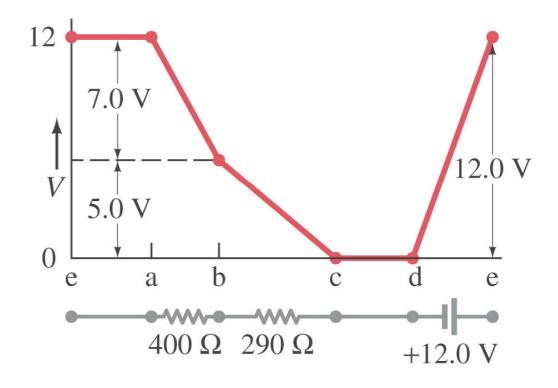
currents leaving it.

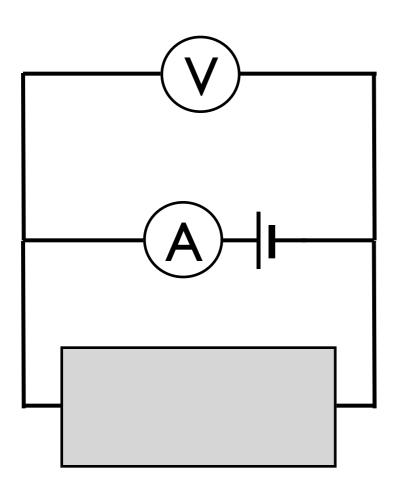


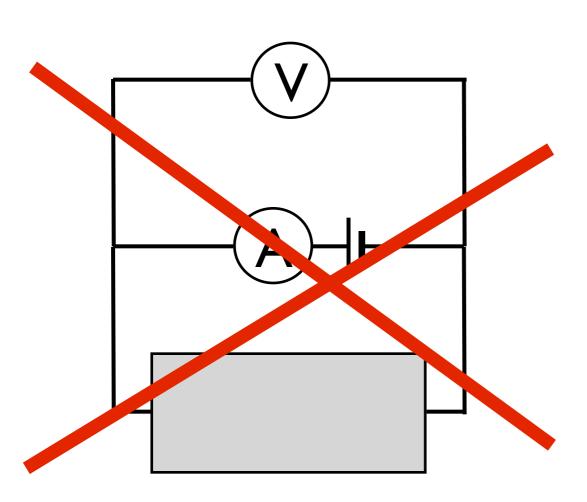
e.g. at a,  $I_3 = I_1 + I_2$ 

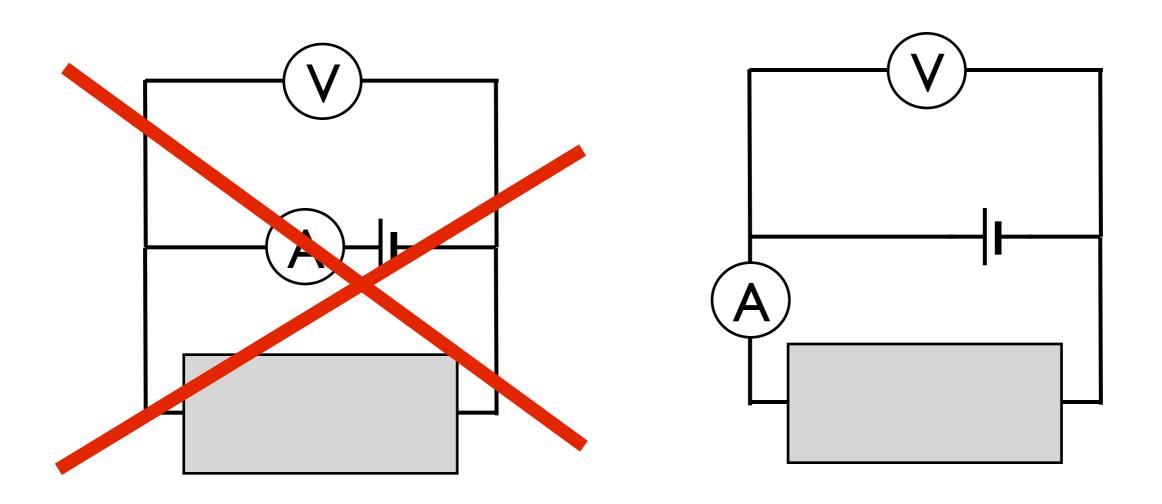
**Loop rule:** The sum of the changes in potential around a closed loop is zero.

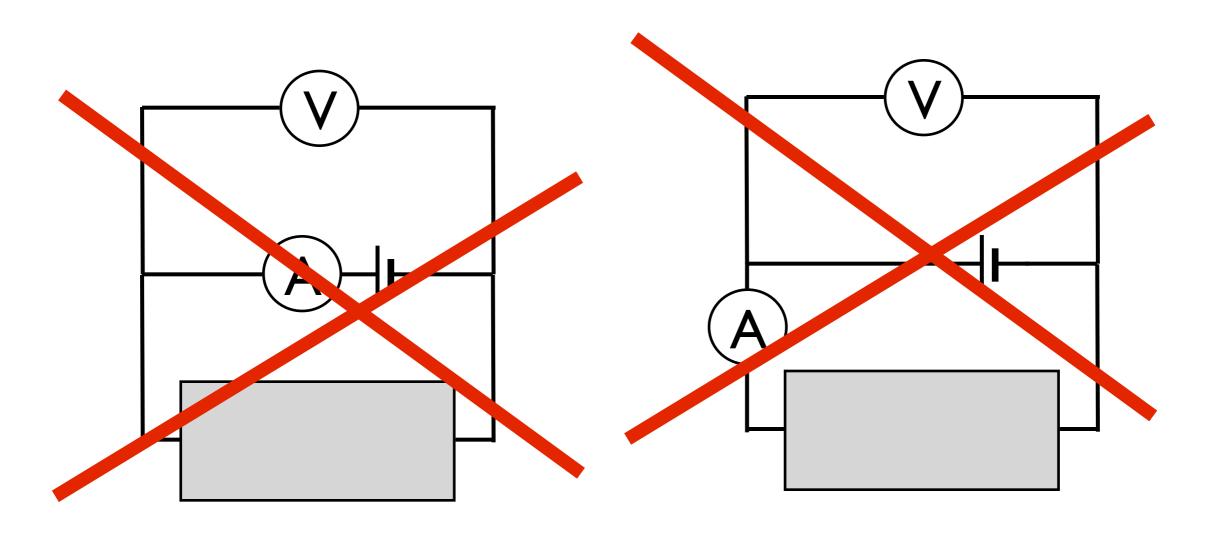




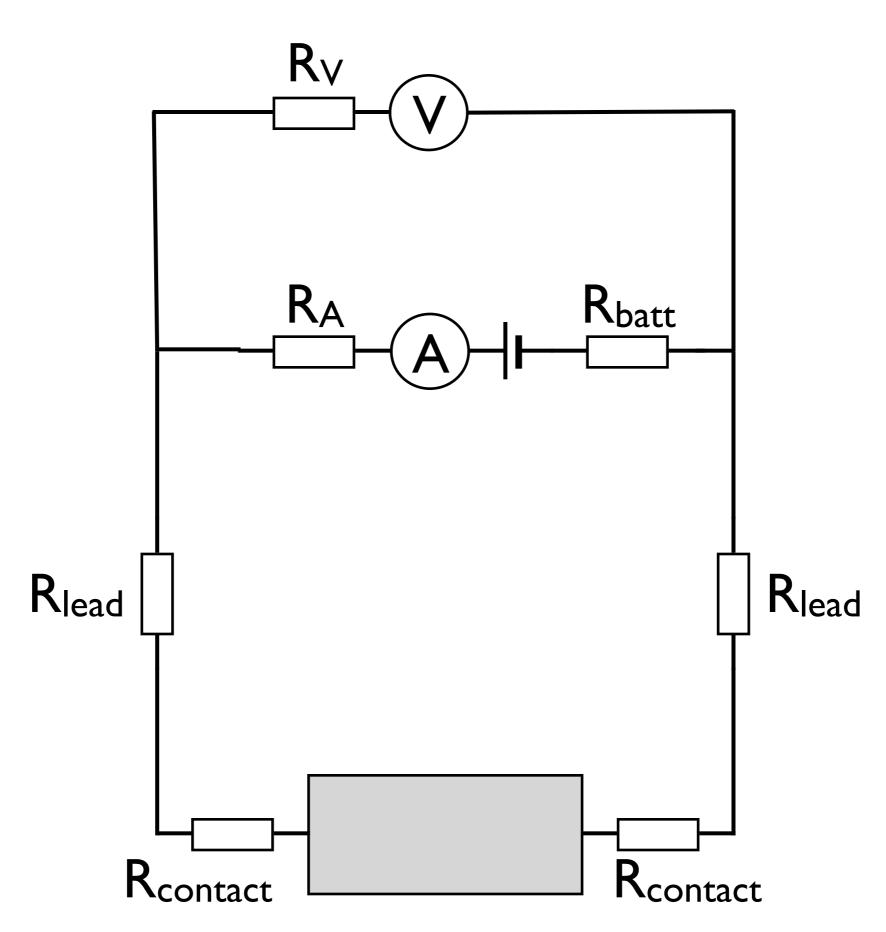




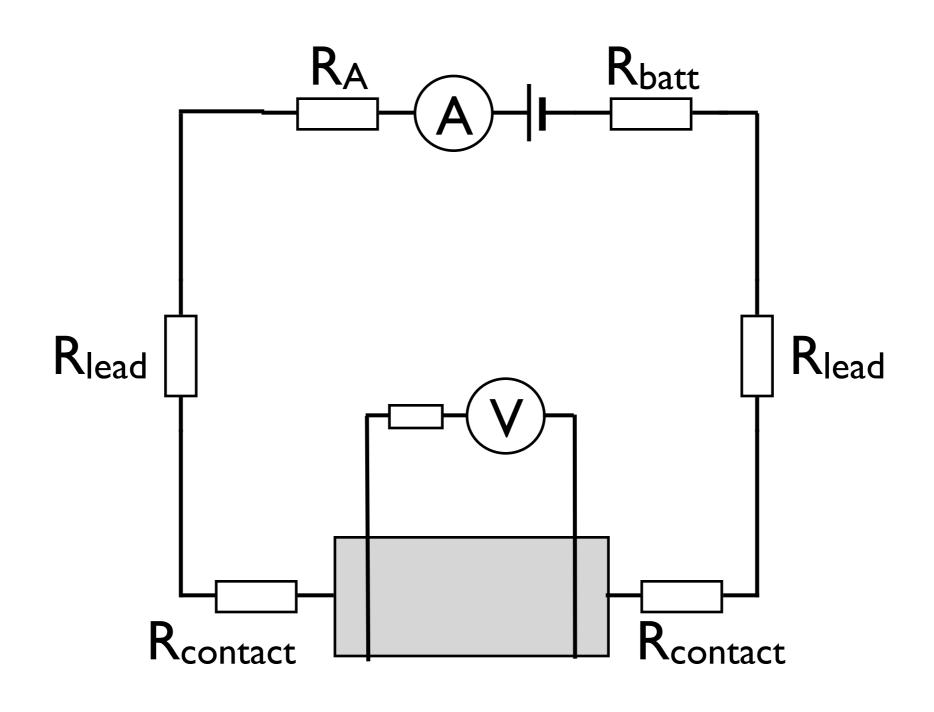


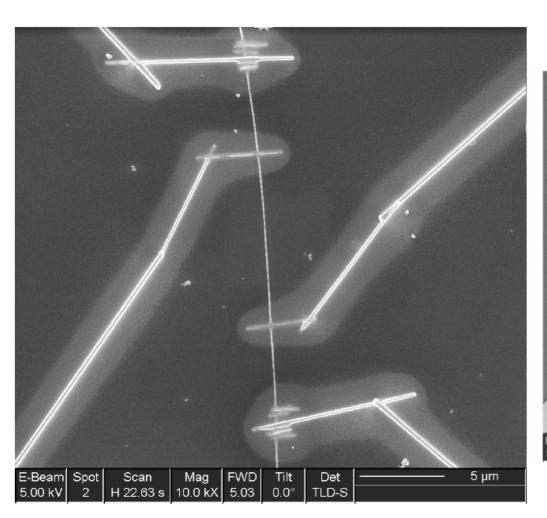


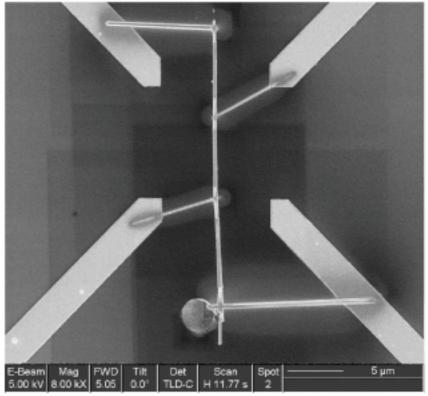
### Real circuit



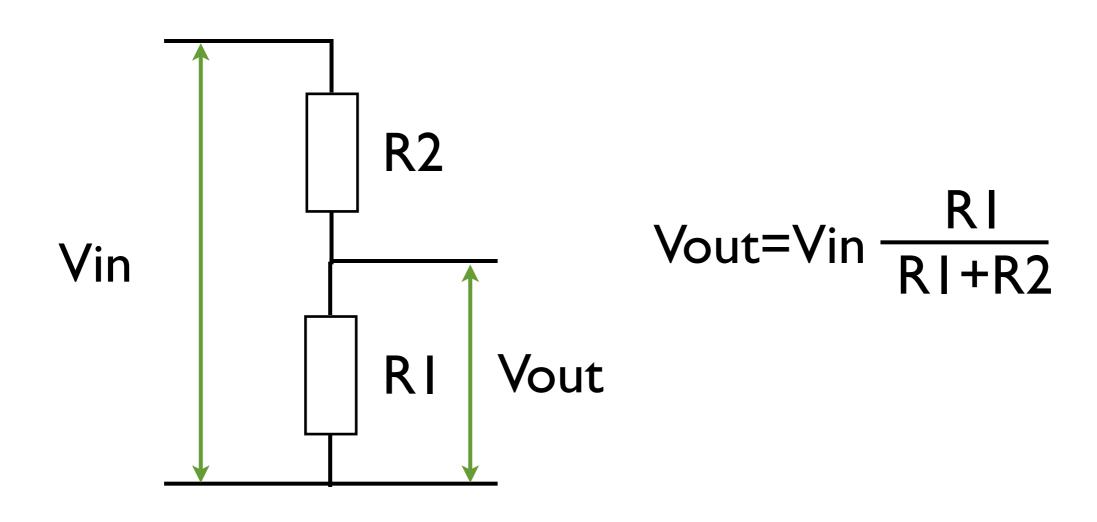
### Solution: four point contact measurement





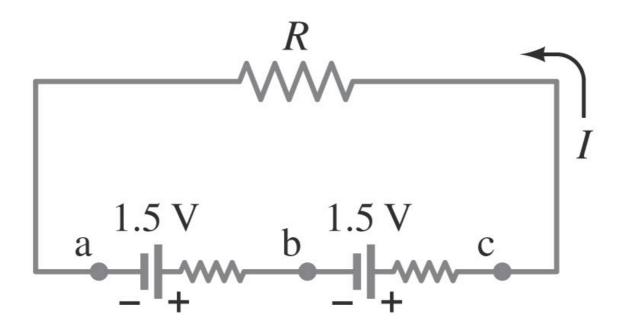


### Simplest circuit: voltage divider

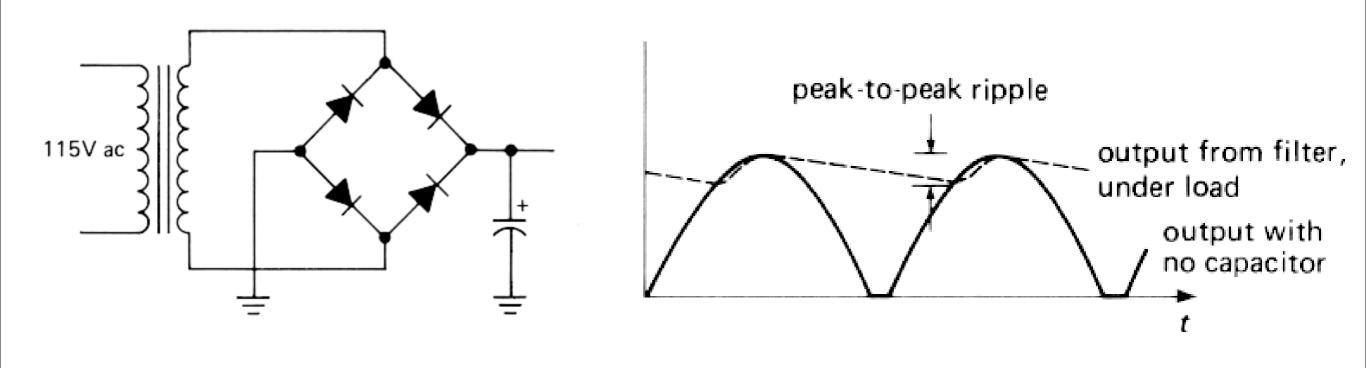


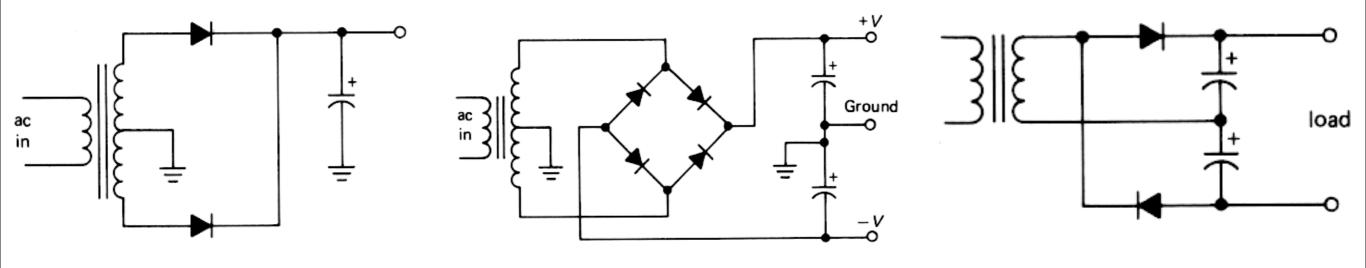
## Batteries





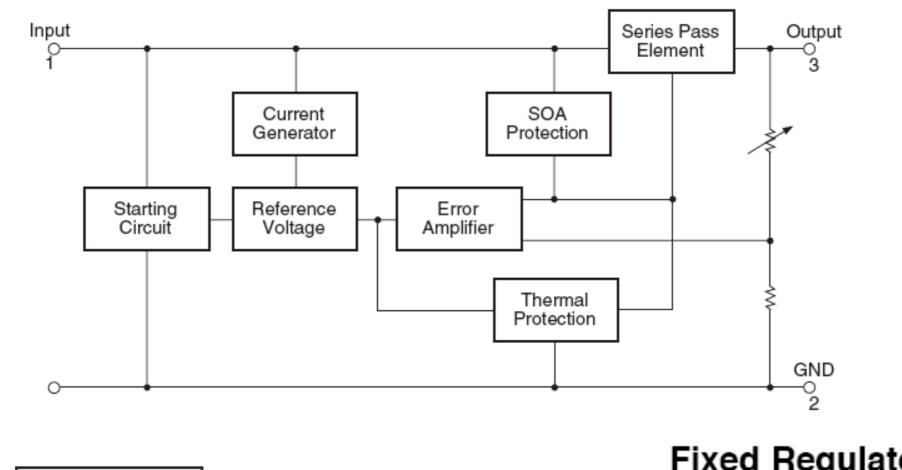
# Analog power supplies

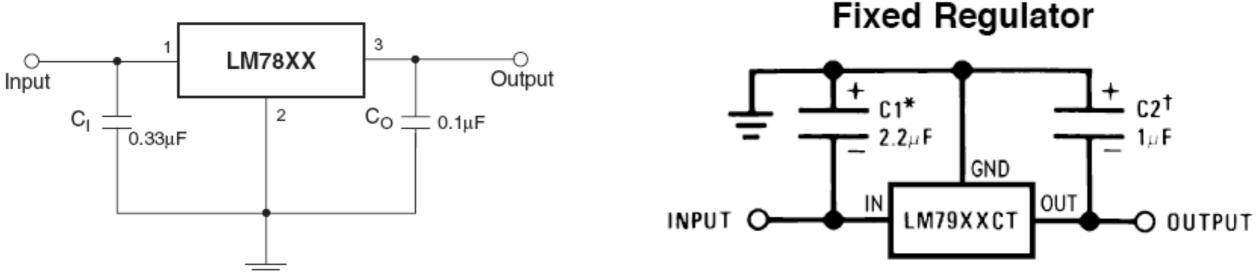




## Voltage regulators

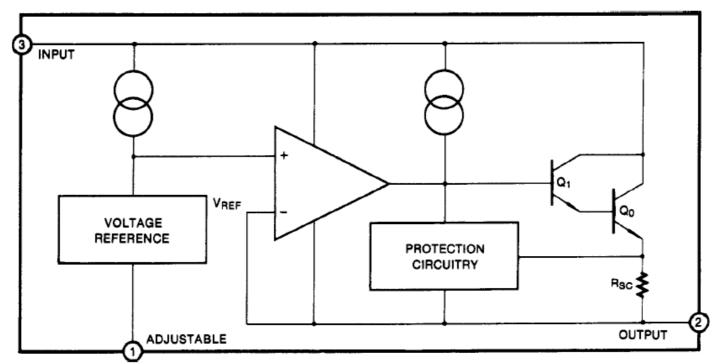
#### **Block Diagram**



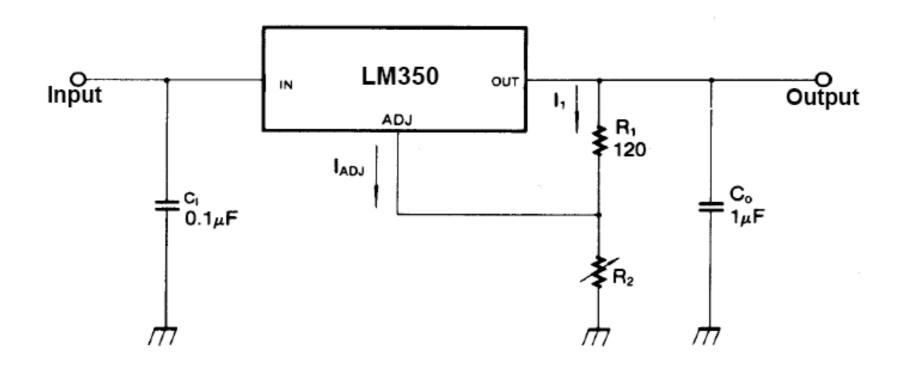


XX=05, 06, 08, 09, 10, 12, 15, 18, 24V

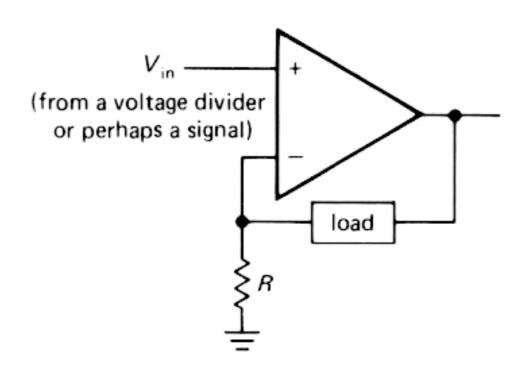
## Adjustable voltage regulators

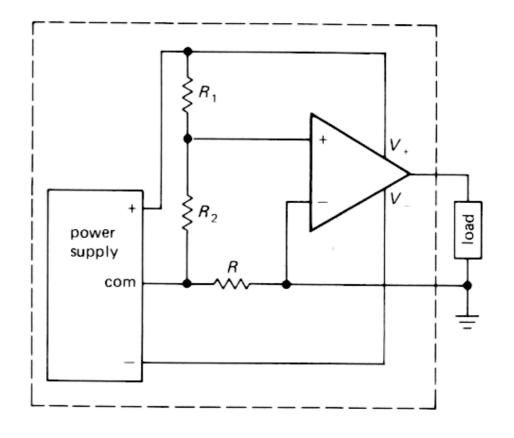


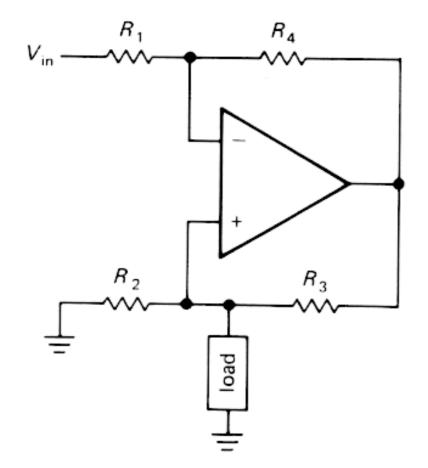
LM350: 3V-35V



### Current sources

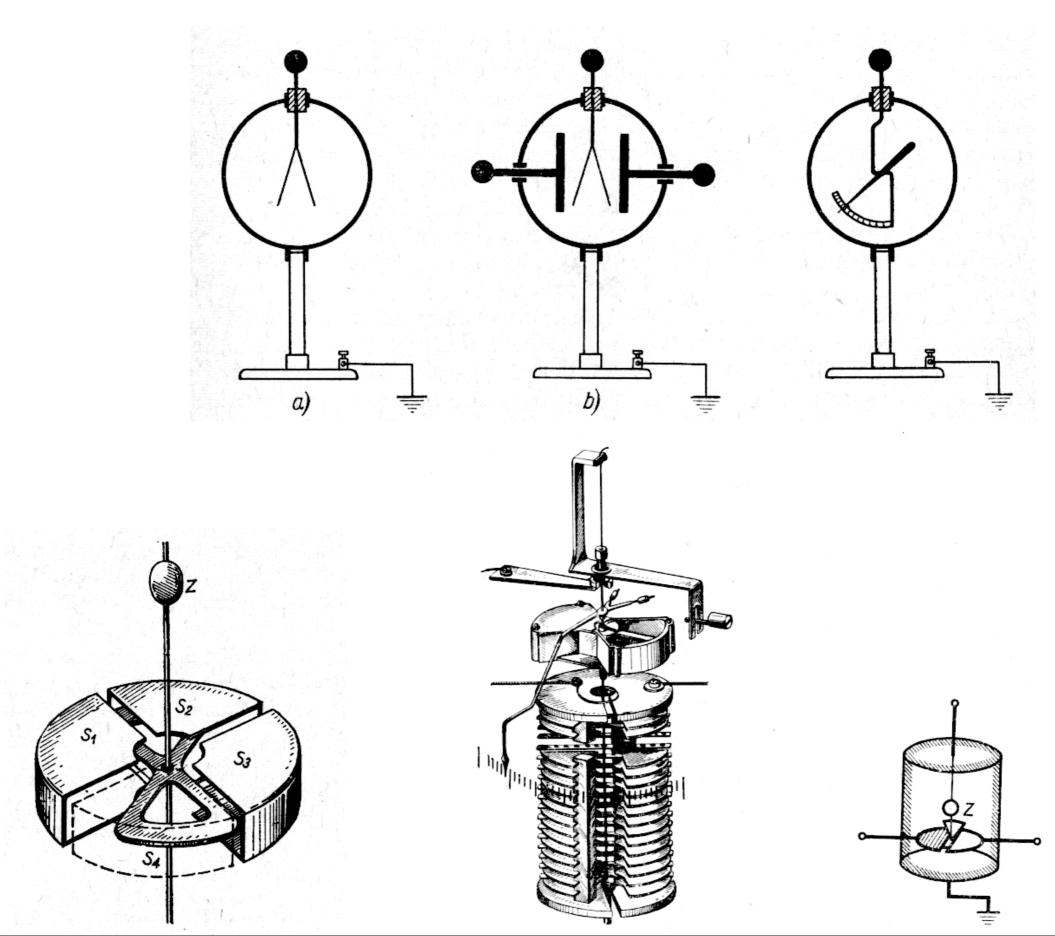




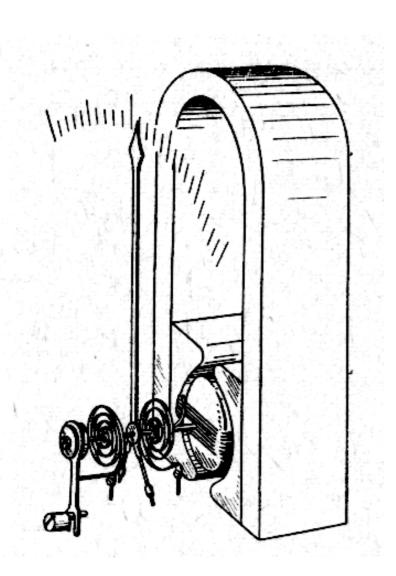


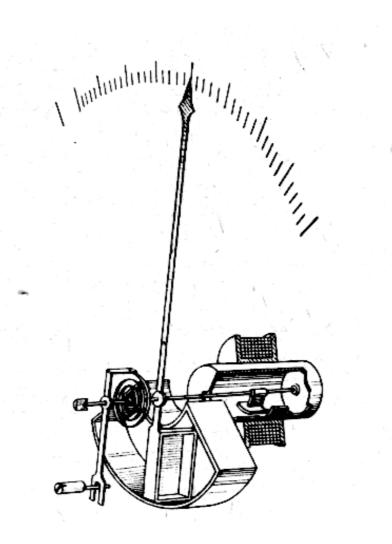


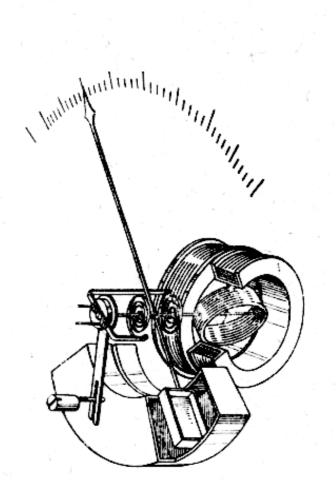
### Electrometers



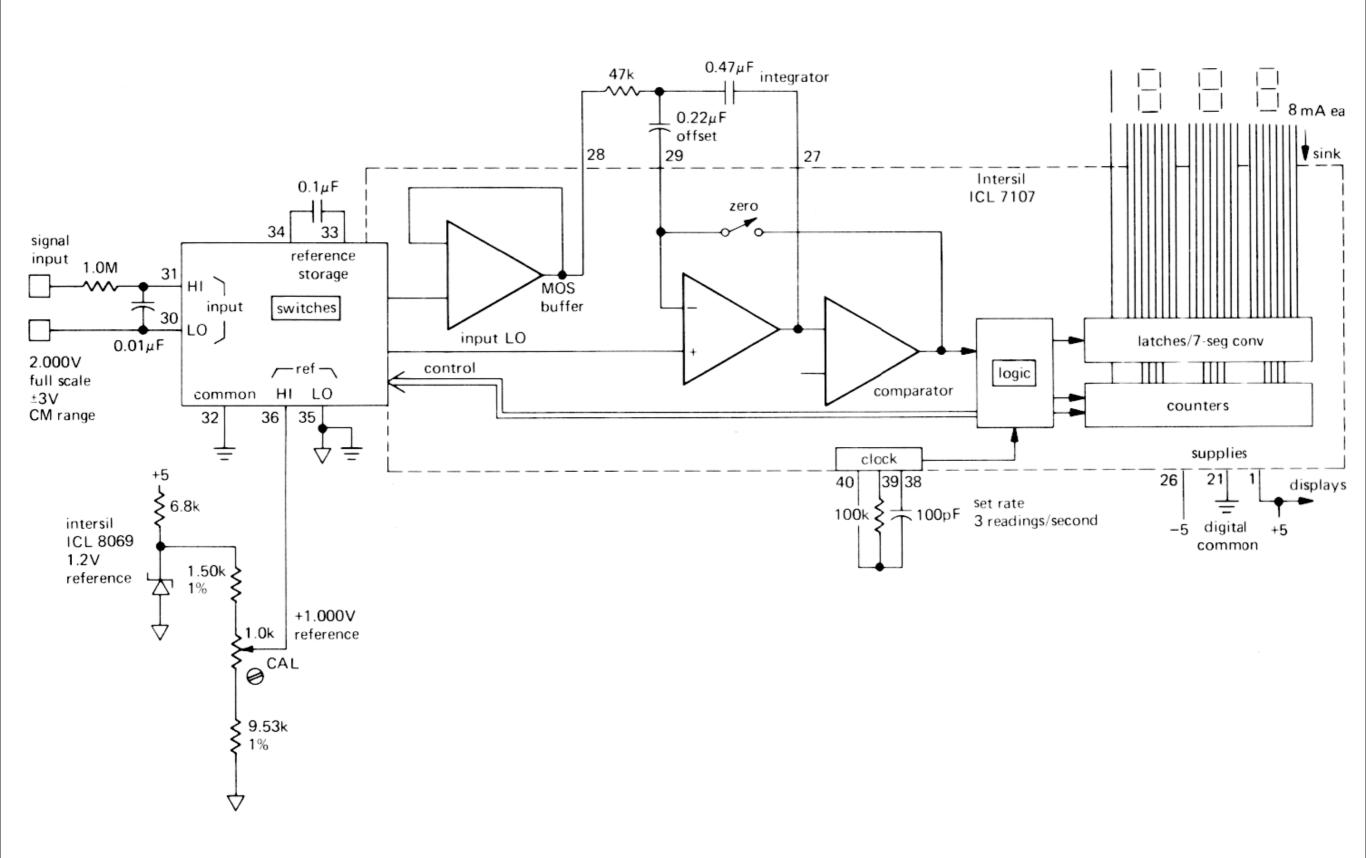
### Current meters



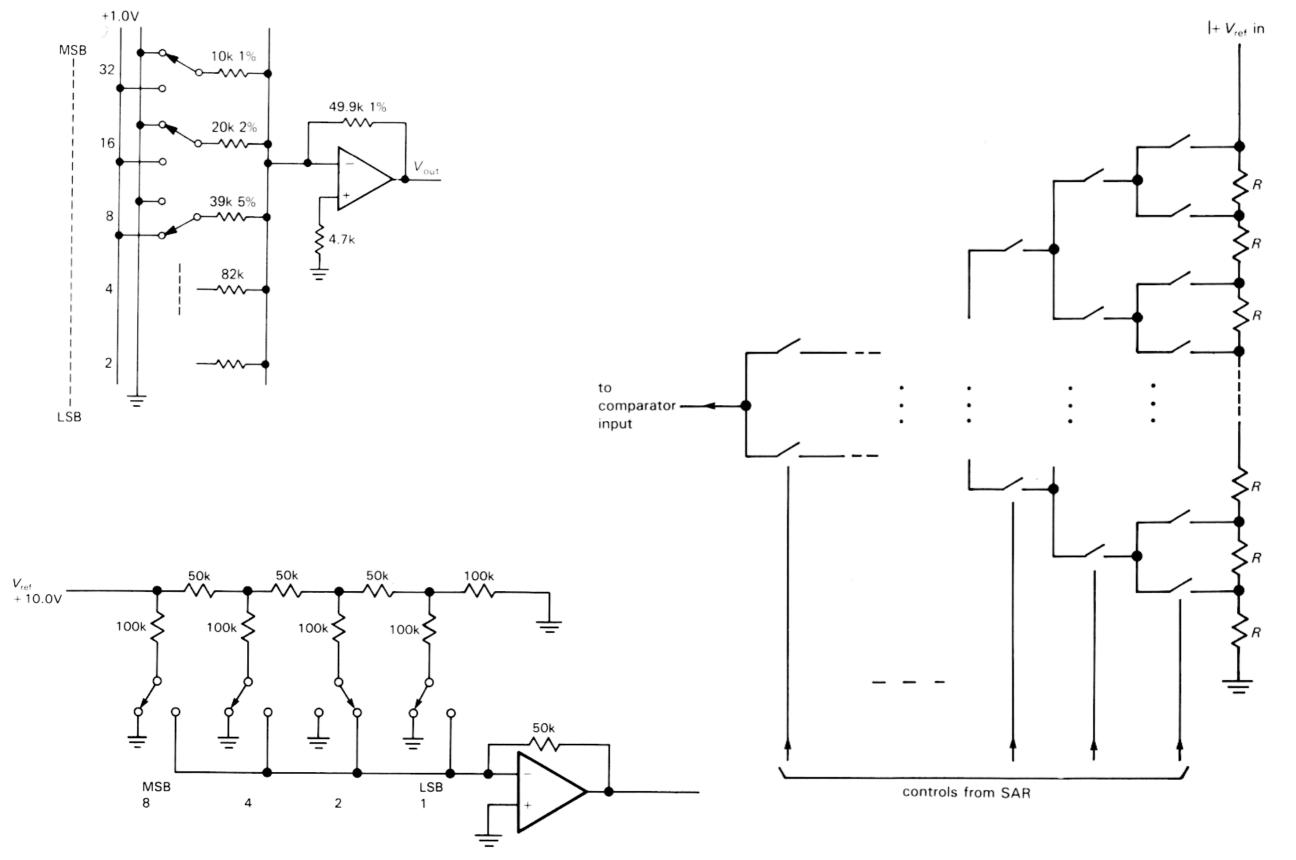




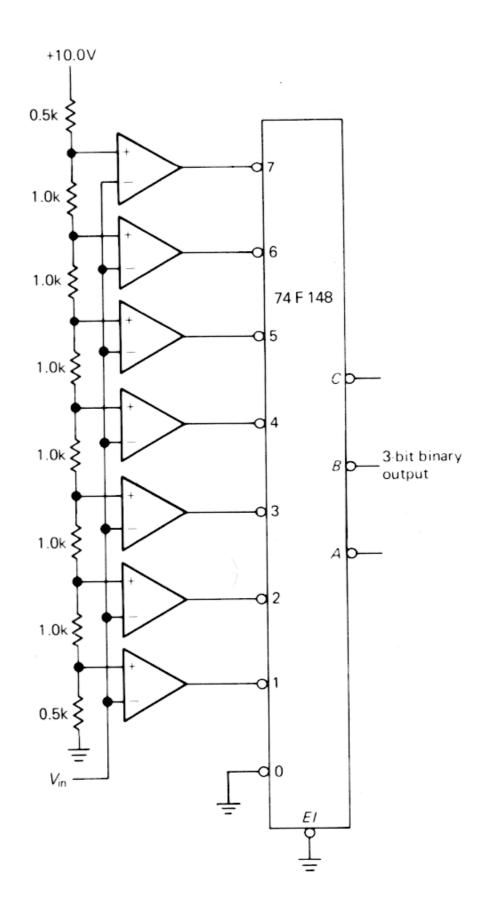
### Digital volt meter (DVM)



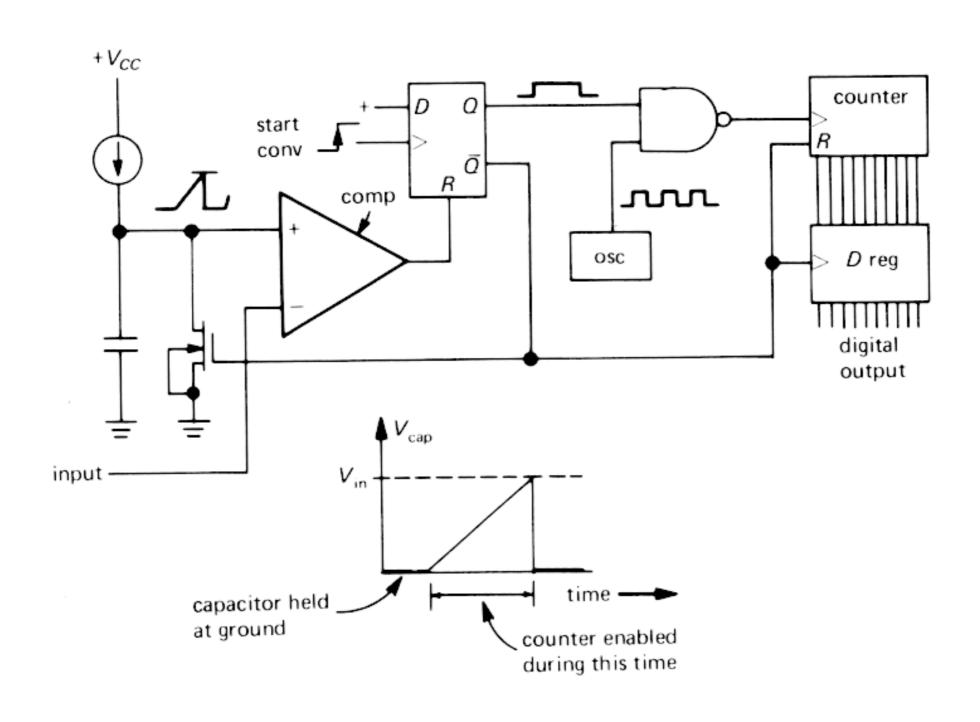
### Digital to analog converters (DAC)



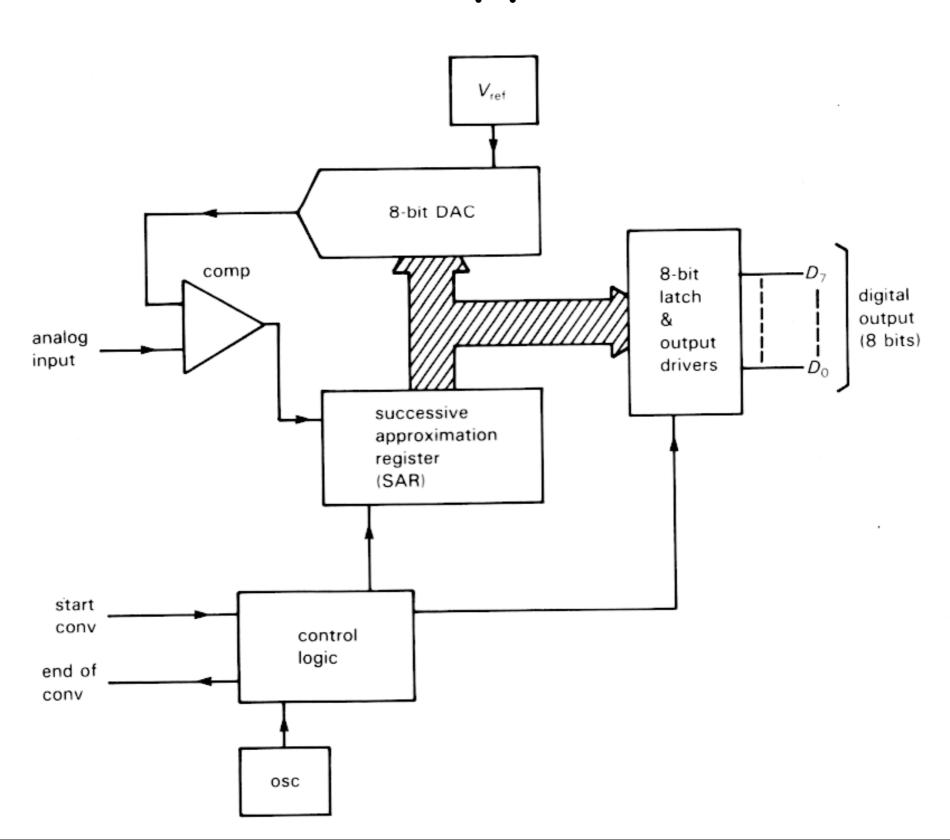
### Analog to digital converters (ADC) - flash



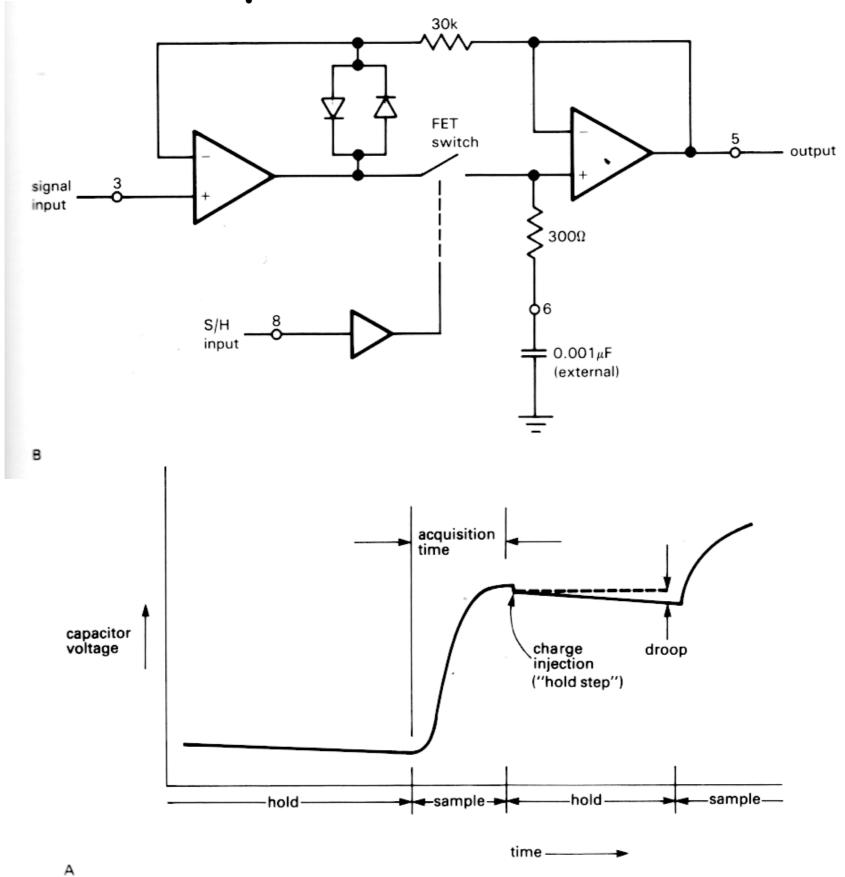
# Analog to digital converters (ADC) "voltage to time"



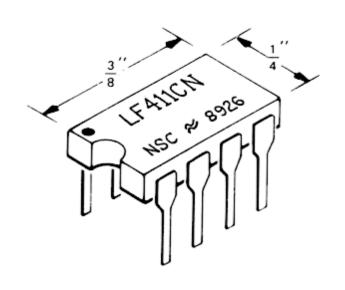
# Analog to digital converters (ADC) "successive approximation"

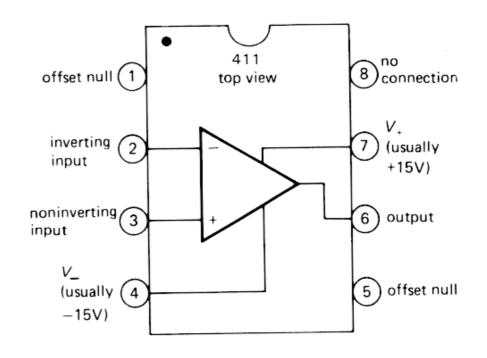


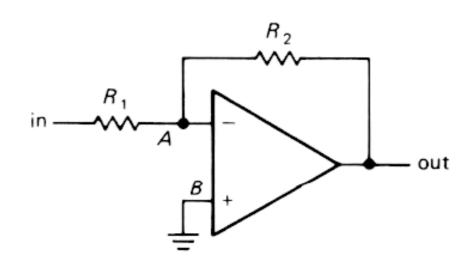
### "Sample and hold circuit"

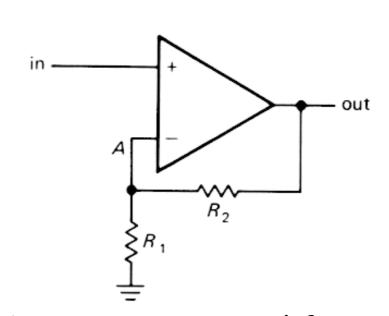


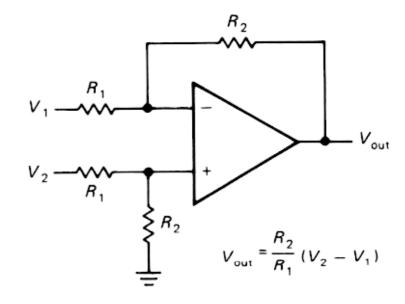
### Signal amplification (OpAmps)









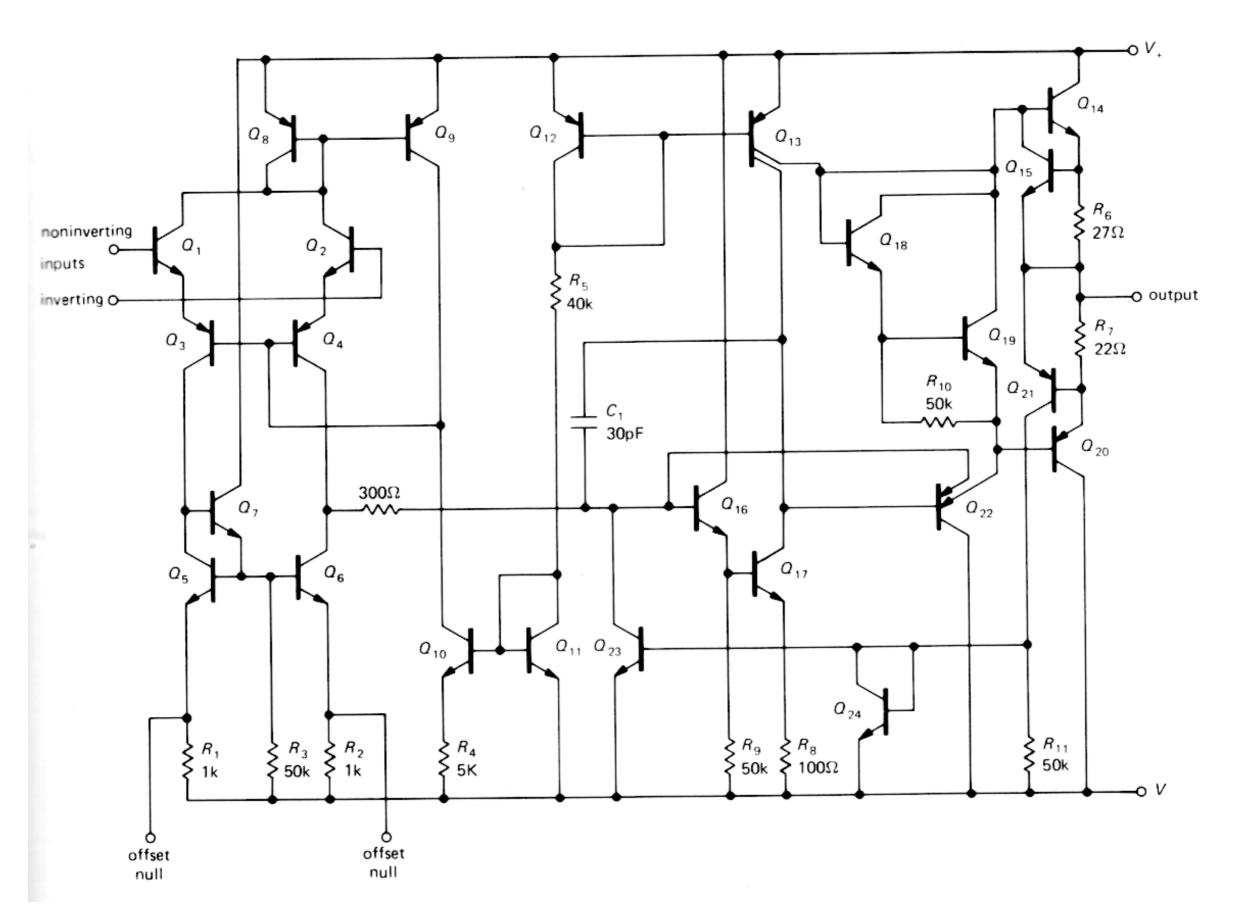


Inverting amplifier

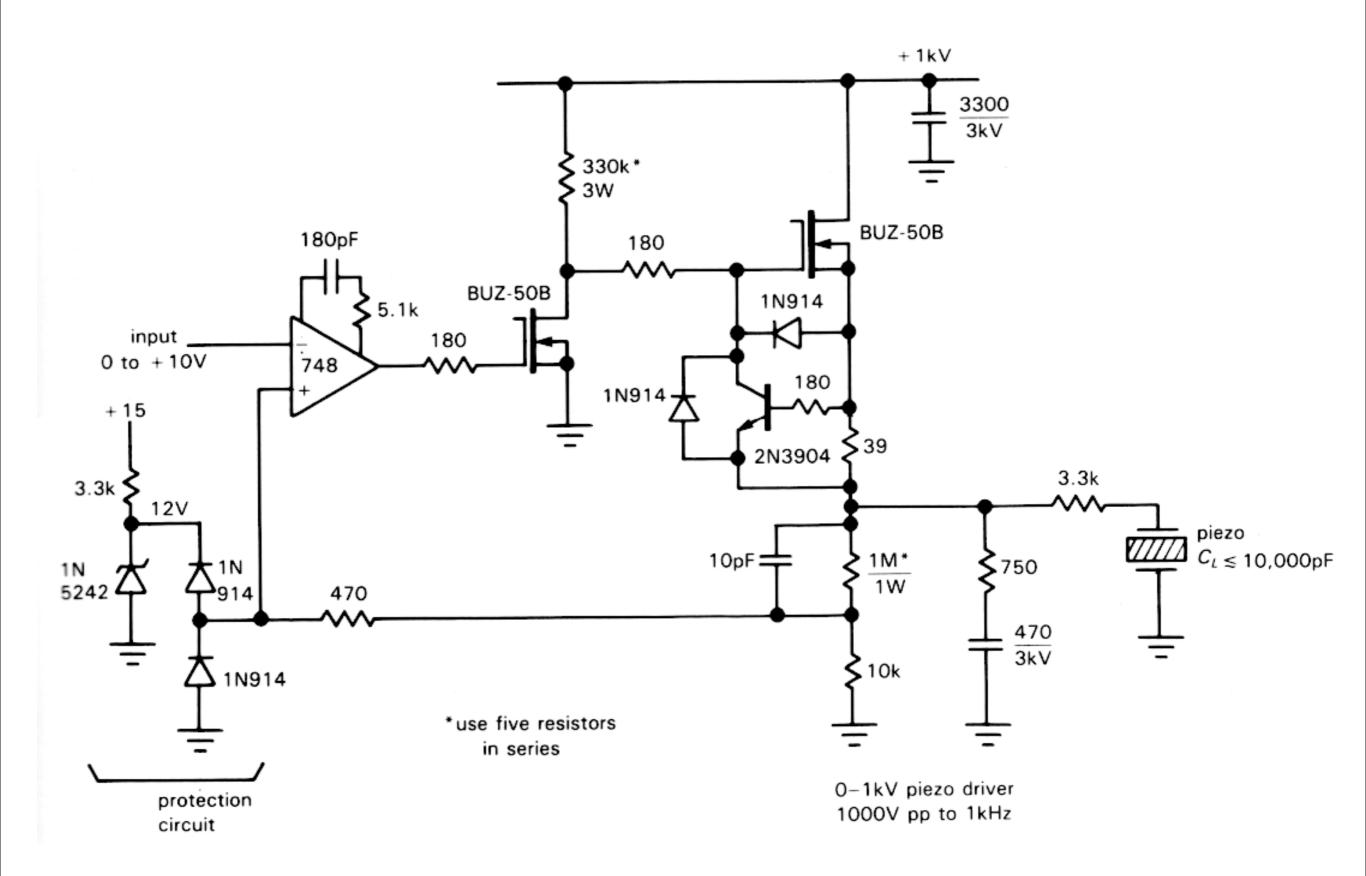
Non-inverting amplifier - "voltage follower"

Differential amplifier

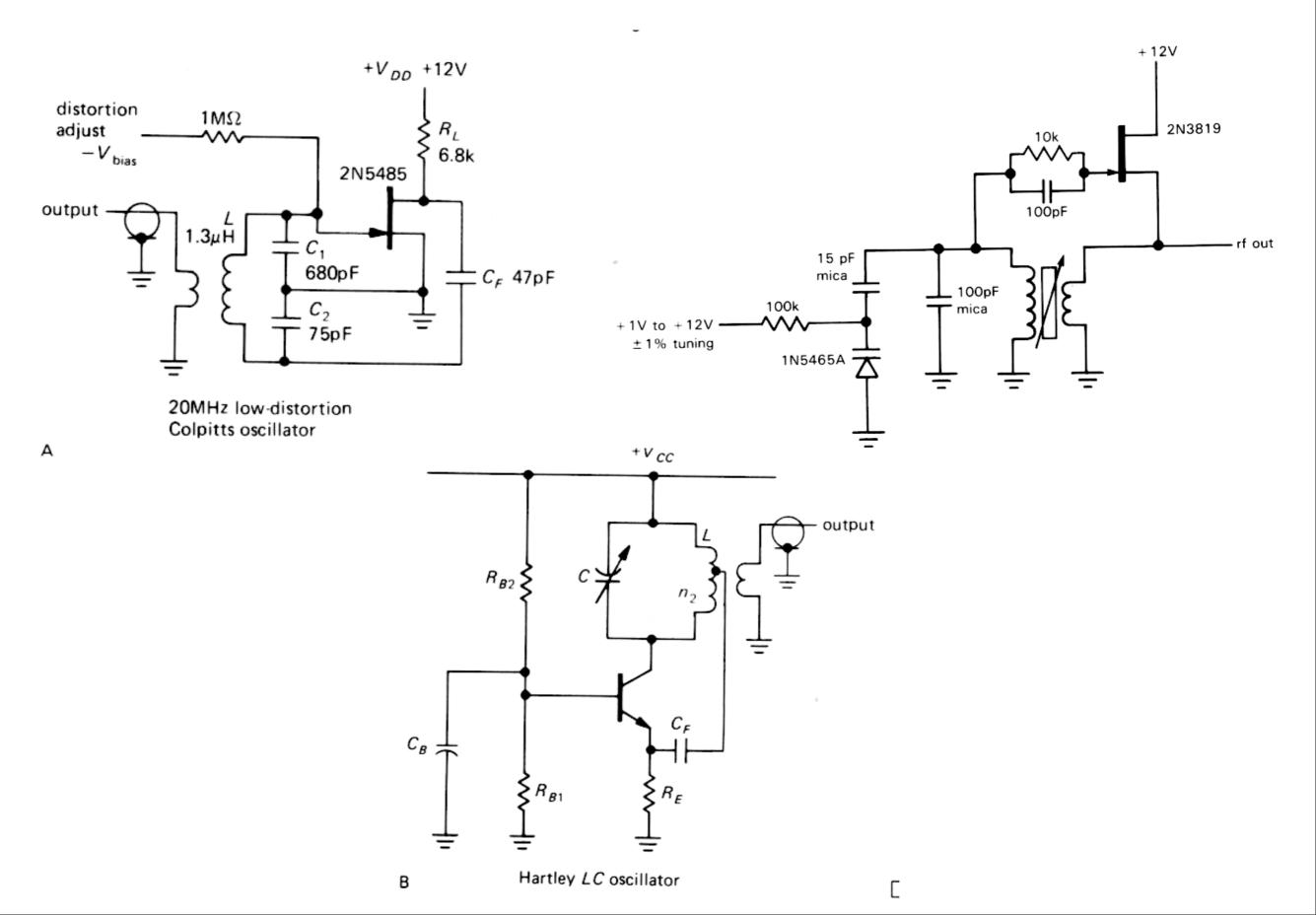
### LF411 internal schematics



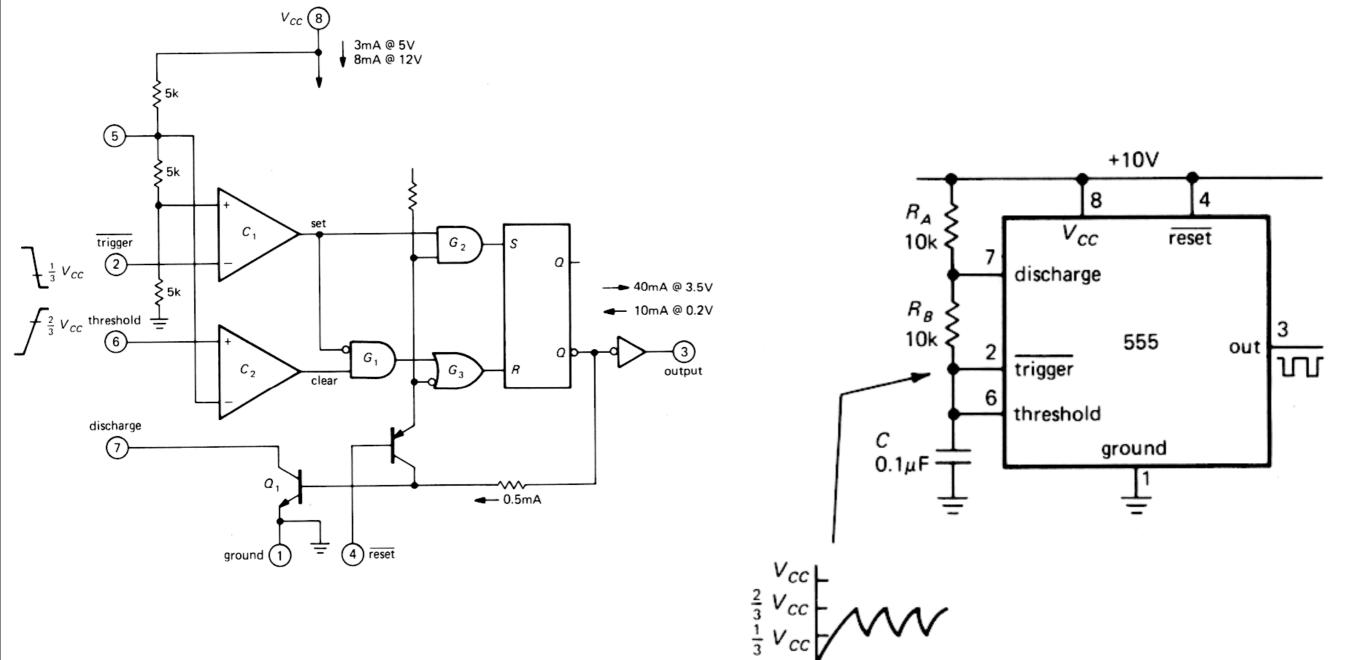
### Example - 1kV piezo driver (2V/ $\mu$ s)



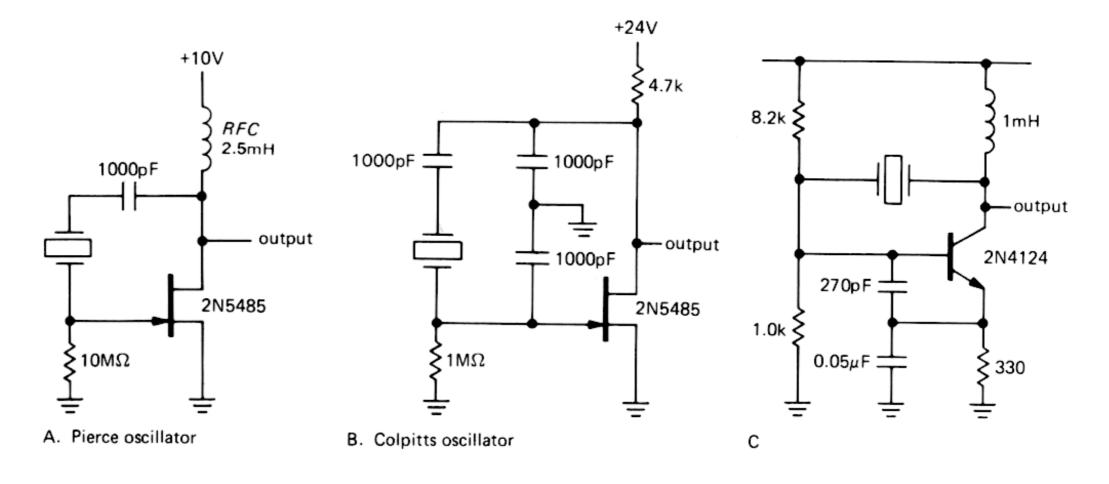
### Oscillators

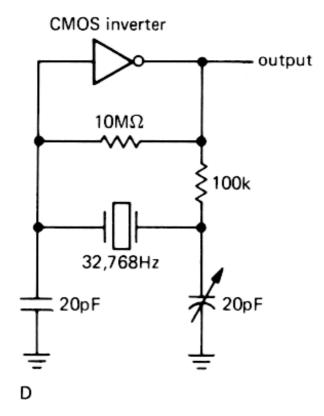


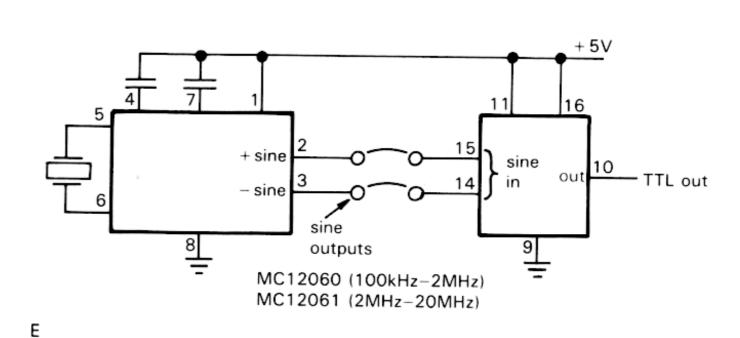
### Single chip oscillator



### Quartz oscillators







### Noise reduction techniques

#### Main sources of noise:

1) interference excitation of currents in circuit due to EM waves

2) Johnson noise - thermal excitation in resistors:

$$V_J = (4kTRB)^{1/2}$$

e. g. T=300, R=10k $\Omega$ : V<sub>J</sub>=1.3  $\mu$  V

3) shot noise (due to quantization of charge)

$$I=(2eI_{dc}B)^{1/2}$$

e. g. at 10 kHz bandwidth

1A current has noise of 57 nA (0.000006%)

 $1\mu$  A current has noise of 6 nA (0.006%)

1pA current has noise of 56 fA (5.6%)

### 4) flicker noise 1/f

#### Resistors

Carbon	composite
--------	-----------

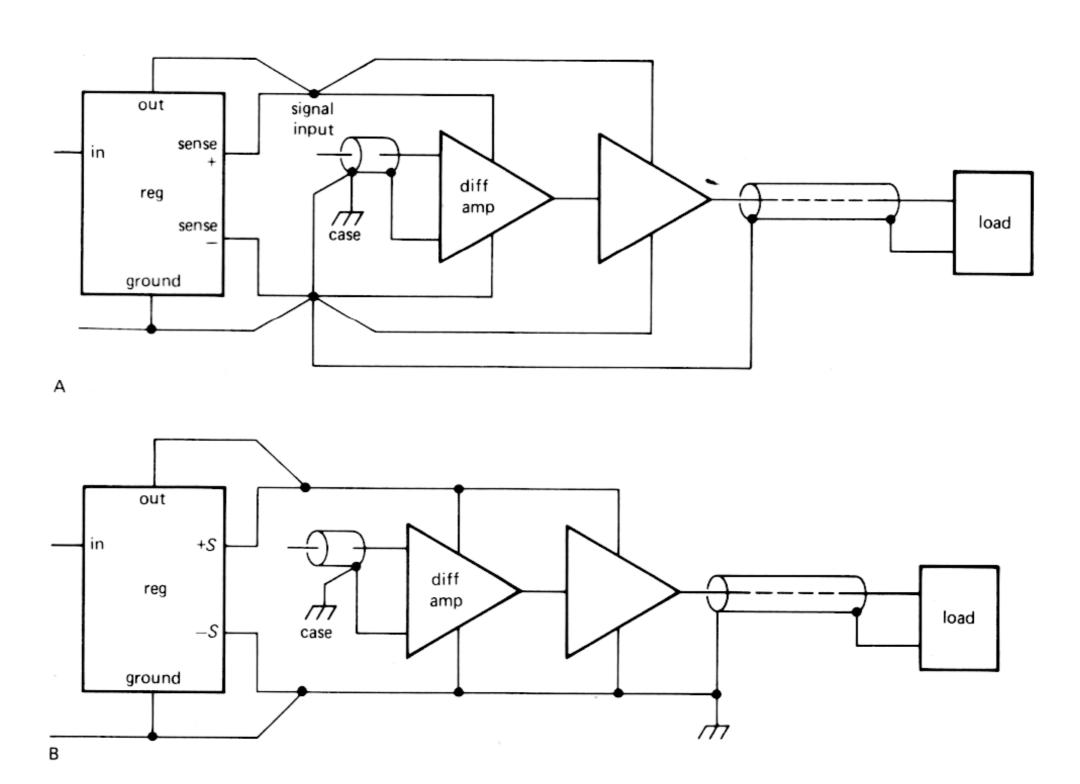
0.1 
$$\mu$$
 V - 3.0  $\mu$  V

$$0.05~\mu\,\mathrm{V}$$
 -  $0.3~\mu\,\mathrm{V}$ 

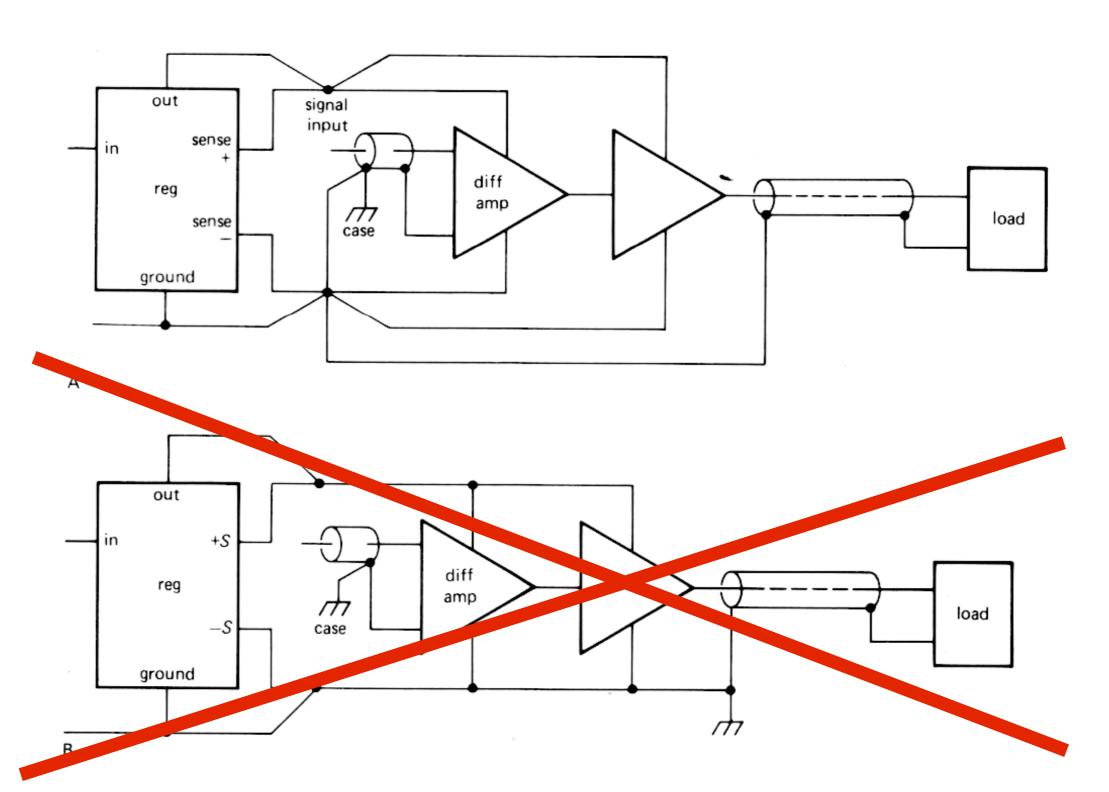
0.02 
$$\mu$$
 V - 0.2  $\mu$  V

0.01 
$$\mu$$
 V - 0.2  $\mu$  V

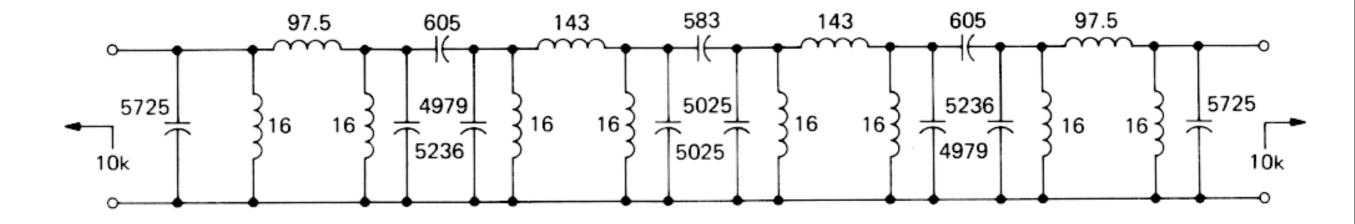
### Grounding

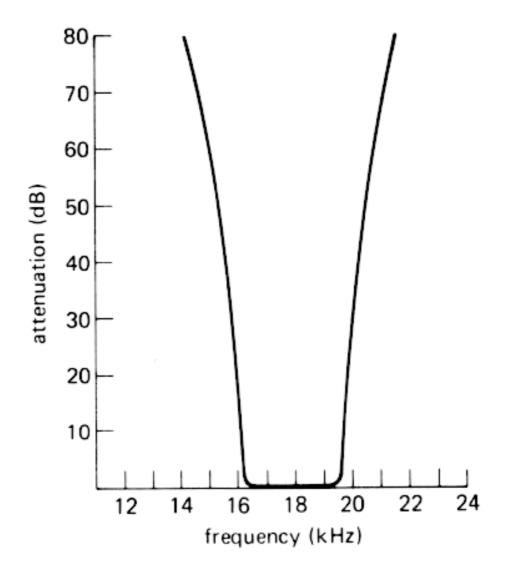


### Grounding

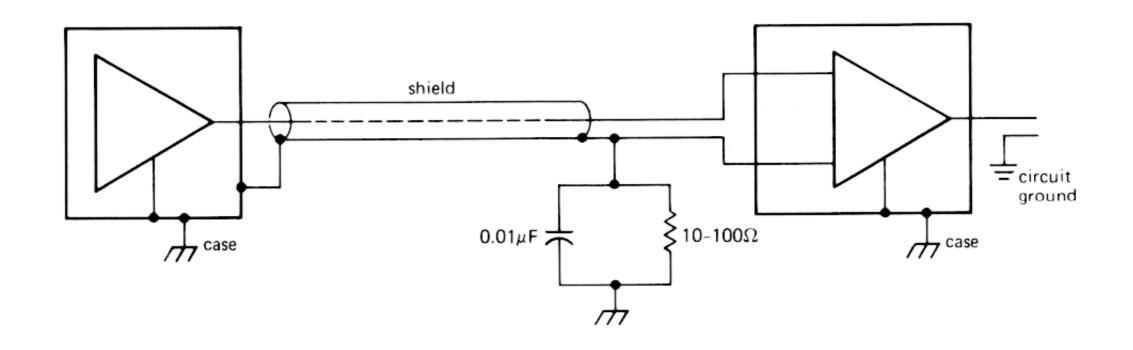


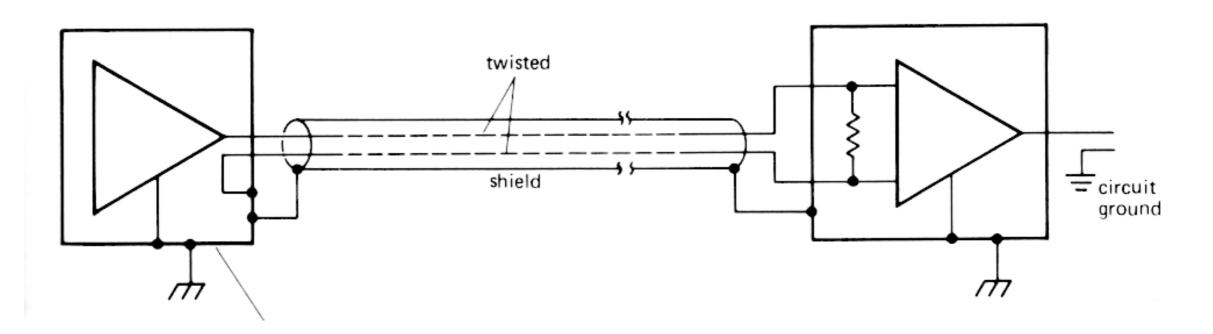
### Filters



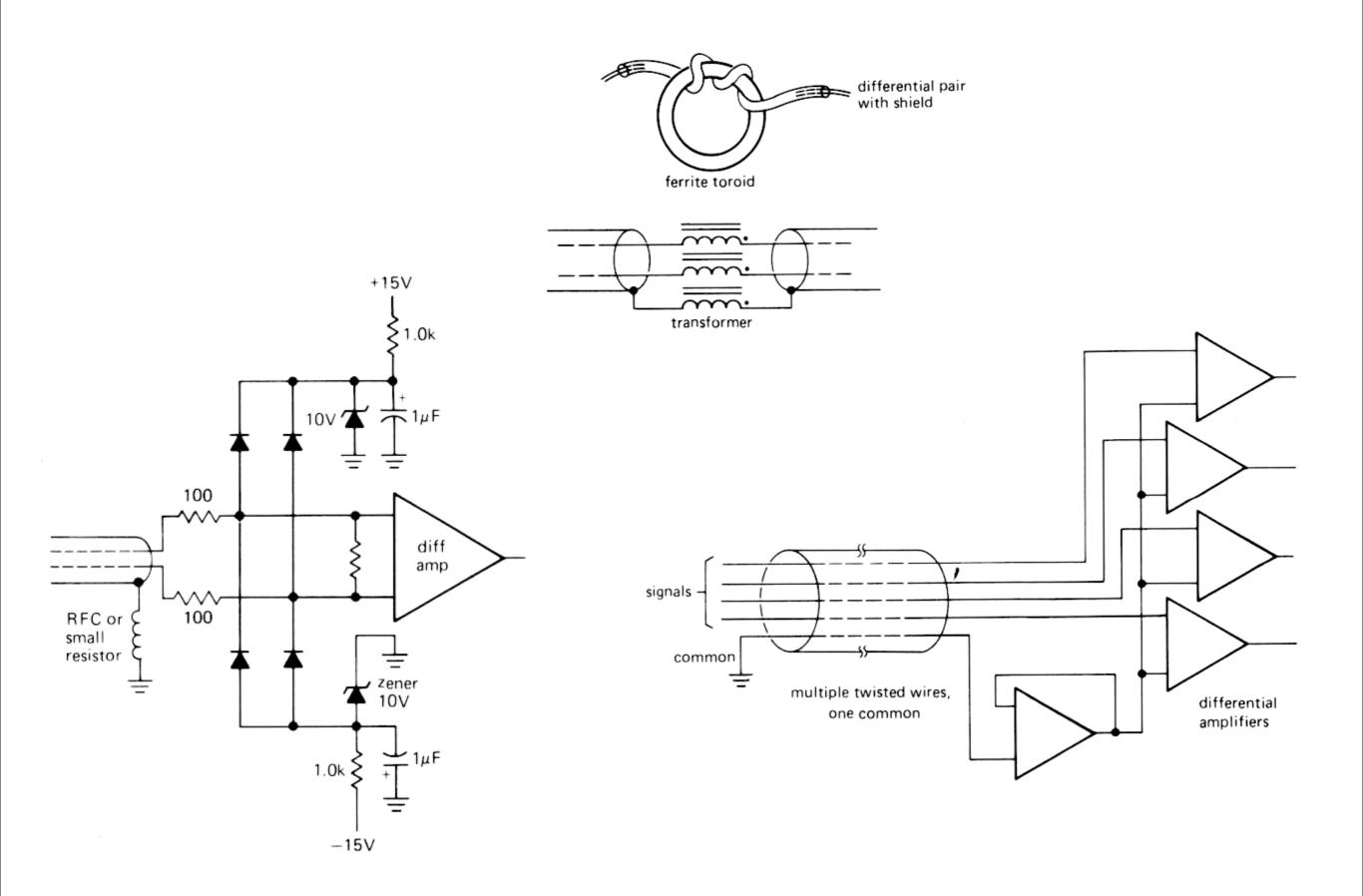


### Shielding

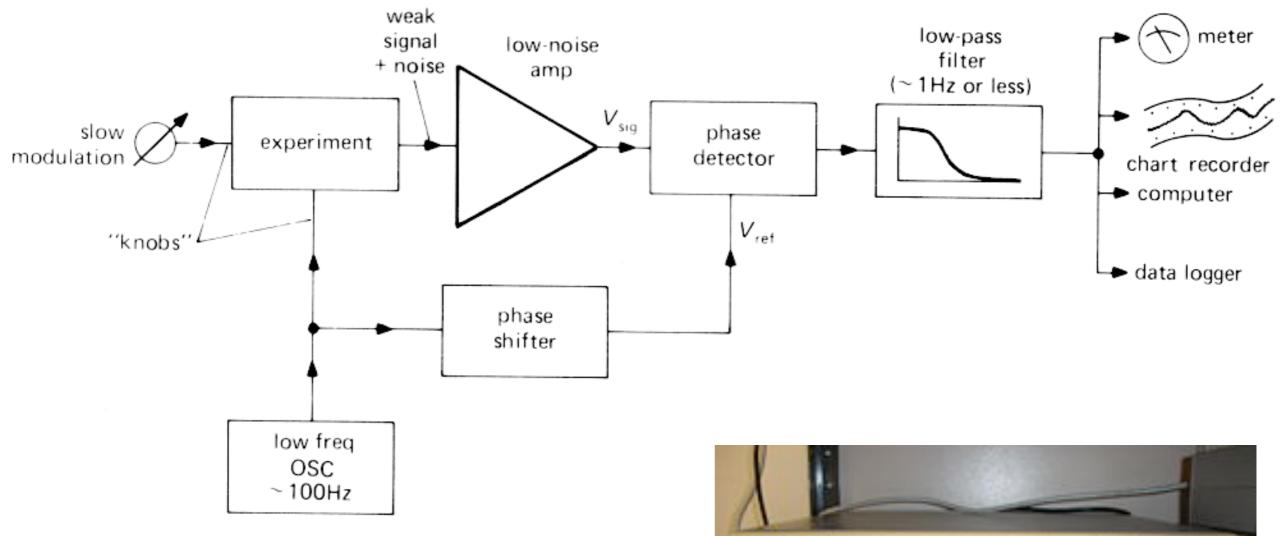




### Noise cancellation



### Lock-in measurements





Signal Recovery



Stanford Research Systems